



Attorney's Docket No. 42390.P7141c

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Patrick Louis-Rene Riffault

Application No.: 09/929,470

Filed: August 13, 2001

For: METHOD AND APPARATUS FOR
MAINTAINING LOAD BALANCE ON A
GRAPHICS BUS WHEN AN UPGRADE
DEVICE IS INSTALLED

)
) Art Unit: 2671
)

)
) Examiner: Unassigned
)

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Technology Center 2600

Assistant Commissioner for Patents
Washington, D.C. 20231

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Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the documents cited on that form. It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant.

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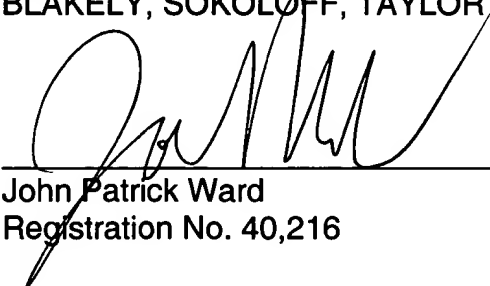
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 3/28, 2002



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Serial/Patent No.: 09/929,470

Client: Intel Corporation

Filing/Issue Date: August 13, 2001

Title: Method and Apparatus for Maintaining Load Balance on a Graphics Bus When an Upgrade Device is Installed

BSTZ File No.: 042390 P7141c

Atty/Secty Initials: EHT/IPW/jxs/bks

Date Mailed: March 29, 2002

Docket Due Date: ***

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